

PROVISION

EX-1A2

MODEL

SERVICE MANUAL

TABLE OF CONTENTS

SPECIFICATIONS	1
SAFETY INSTRUCTIONS AND MAINTENANCE	2
X-RAY RADIATION PRECAUTION	2
SAFETY PRECAUTION	2
PRODUCT SAFETY NOTICE	3
SAFETY SYMBOL DESCRIPTION	3
MAINTENANCE	4
ADJUSTMENTS	5
SET-UP ADJUSTMENTS	5
CIRCUIT ADJUSTMENTS	8
STRUCTURE AND CHASSIS FUNCTION DESCRIPTION	14
STRUCTURE BLOCK DIAGRAM	14
BLOCK DIAGRAM FOR SUPPLY VOLTAGE SYSTEM	15
CHASSIS DESCRIPTION	16
SERVICE DATA	17
TECHNICAL DATA OF KEY ICS	17
WAVEFORMS OF KEY POINTS	35
SERVICE DATA OF KEY ICS	37
REPLACEMENT OF PARTS	41

APPENDIX

1. CIRCUIT DIAGRAM
2. PRINTED CIRCUIT BOARD DIAGRAMS
3. FINAL WIRING DIAGRAMS
4. FINAL ASSEMBLY DIAGRAMS

SERVICE MANUAL

SPECIFICATIONS

Model Number		21C99	PF21C99	PF21C18
RF system	Color system	PAL4.43, NTSC3.58, NTSC4.43, SECAM		
	Sound system	D/K, I, M, B/G		
Video system		PAL4.43, NTSC3.58, NTSC4.43, PAL-M, PAL-N (50/60Hz)		
Receiving channel	VHF	C1- C12 (49.75-85.25MHz, 168.25-216.25MHz)		
	UHF	C13-C57 (471.25-863.25MHz)		
	CATV	Z1-Z7 (111.0-187.0MHz) Z8-Z35 (223.0-447.0MHz)		
Channels preset		236(0-235)		
Antenna input		75 Ω (unbalanced)		
Picture tube Effective screen dimensions (Approx.)		406 \times 305mm		
Audio output (THD \leq 7%)		3W+3W		
Power source		110-240V ~, 50/60Hz		
Weight (Approx.)		23kg	25kg	26kg
Dimensions (W \times H \times D) (Approx.)		585 \times 460 \times 495mm		585 \times 461 \times 485mm
Rated power consumption		69W		

Designs and specifications are subject to change without notice.

INSTRUCTIONS FOR SERVICE SAFETY AND MAINTENANCE

WARNING: BEFORE SERVICING THIS CHASSIS, READ THE "X-RAY RADIATION PRECAUTION", "SAFETY PRECAUTION" AND "PRODUCT SAFETY NOTICE" INSTRUCTION BELOW.

X-RAY RADIATION PRECAUTION

1. The EHT must be checked every time the TV is serviced to ensure that the CRT does not emit X-ray radiation as result of excessive EHT voltage. The maximum EHT voltage permissible in any operating circumstances must not exceed the rated value. When checking the EHT, use the High Voltage Check procedure in this manual using an accurate EHT voltmeter.
2. The only source of X-RAY radiation in this TV is the CRT. The TV minimizes X-RAY radiation, which ensures safety during normal operation. To prevent X-ray radiation, the replacement CRT must be identical to the original fitted as specified in the parts list.
3. Some components used in this TV have safety related characteristics preventing the CRT from emitting X-ray radiation. For continued safety, replacement component should be made after referring the PRODUCT SAFETY NOTICE below.
4. Service and adjustment of the TV may result in changes in the nominal EHT voltage of the CRT anode. So ensure that the maximum EHT voltage does not exceed the rated value after service and adjustment.

SAFETY PRECAUTION

WARNING: REFER SERVICING TO QUALIFIED SERVICE PERSONNEL ONLY.

1. The TV has a nominal working EHT voltage. Extreme caution should be exercised when working on the TV with the back removed.
 - 1.1 Do not attempt to service this TV if you are not conversant with the precautions and procedures for working on high voltage equipment.
 - 1.2 When handling or working on the CRT, always discharge the anode to the TV chassis before removing the anode cap in case of electric shock.
 - 1.3 The CRT, if broken, will violently expel glass fragments. Use shatterproof goggles and take extreme care while handling.
 - 1.4 Do not hold the CRT by the neck as this is a very dangerous practice.
2. It is essential that to maintain the safety of the customer all power cord forms be replaced exactly as supplied from factory.
3. Voltage exists between the hot and cold ground when the TV is in operation. Install a suitable isolating transformer of beyond rated overall power when servicing or connecting any test equipment for the sake of safety.
4. When replacing ICs, use specific tools or a static-proof electric iron with small power (below 35W).
5. Do not use a magnetized screwdriver when tightening or loosening the deflection yoke assembly to

- avoid electronic gun magnetized and decrement in convergence of the CRT.
6. When remounting the TV chassis, ensure that all guard devices, such as nonmetal control buttons, switch, insulating sleeve, shielding cover, isolating resistors and capacitors, are installed on the original place.
7. Replace blown fuses within the TV with the fuse specified in the parts list.
8. When replacing wires or components to terminals or tags, wind the leads around the terminal before soldering. When replacing safety components identified by the international hazard symbols on the circuit diagram and parts list, it must be the company-approved type and must be mounted as the original.
9. Keep wires away from high temperature components.

PRODUCT SAFETY NOTICE

CAUTION: FOR YOUR PROTECTION, THE FOLLOWING PRODUCT SAFETY NOTICE SHOULD BE READ CAREFULLY BEFORE OPERATING AND SERVICING THIS TV SET.

1. Many electrical and mechanical components in this chassis have special safety-related characteristics. These characteristics are often passed unnoticed by a visual inspection and the X-ray radiation protection afforded by them cannot necessarily be obtained by using replacements rated at higher voltages or wattage, etc. Components which have these special safety characteristics in this manual and its supplements are identified by the international hazard symbols on the circuit diagram and parts list. Before replacing any of these components read the parts list in this manual carefully. Substitute replacement components which do not have the same safety characteristics as specified in the parts list may create X-ray radiation.
2. Do not slap or beat the cabinet or CRT, since this may result in fire or explosion.
3. Never allow the TV sharing a plug or socket with other large-power equipment. Doing so may result in too large load, causing fire.
4. Do not allow anything to rest on or roll over the power cord. Protect the power cord from being walked on, modified, cut or pinched, particularly at plugs.
5. Do not place any objects, especially heavy objects and lightings, on top of the TV set. Do not install the TV near any heat sources such as radiators, heat registers, stove, or other apparatus that produce heat.
6. Service personnel should observe the SAFETY INSTRUCTIONS in this manual during use and servicing of this TV set. Otherwise, the resulted damage is not protected by the manufacturer.

SAFETY SYMBOL DESCRIPTION

The lightning symbol in the triangle tells you that the voltage inside this product may be strong enough to cause an electric shock. Extreme caution should be exercised when working on the TV with the back removed.

SERVICE MANUAL



This is an international hazard symbol, telling you that the components identified by the symbol have special safety-related characteristics.



FDA This symbol tells you that the critical components identified by the FDA marking have special safety-related characteristics.

UL This symbol tells you that the critical components identified by the UL marking have special safety-related characteristics.

C UL This symbol tells you that the critical components identified by the C-UL marking have been evaluated to the UL and C-UL standards and have special safety-related characteristics.

VDE This symbol tells you that the critical components identified by the VDE marking have special safety-related characteristics.

MAINTENANCE

1. Install the TV set on a stable and level surface. Do not place the set near or over a radiator or heat register, or where it is exposed to direct sunlight.
2. Do not install the TV set in a place exposed to rain, water, excessive dust, mechanical vibrations or impacts.
3. Allow enough space (at least 10cm) between the TV and wall or enclosures for proper ventilation.
4. Slots and openings in the cabinet should never be blocked by clothes or other objects.
5. Please power off the TV set and disconnect it from the wall immediately if any abnormal conditions are met, such as bad smell, belching smoke, sparking, abnormal sound, no picture/sound, etc. Hold the plug firmly when disconnecting the power cord.
6. Unplug the TV set from the wall outlet before cleaning or polishing it. Use a dry soft cloth for cleaning the exterior of the TV set or CRT screen. Do not use liquid cleaners or aerosol cleaners.

SERVICE MANUAL

ADJUSTMENTS

SET-UP ADJUSTMENTS

The following adjustments should be made when a complete realignment is required or a new picture tube is installed.

Perform the adjustments in the following order:

1. Color purity
2. Convergence
3. White balance

Notes:

①

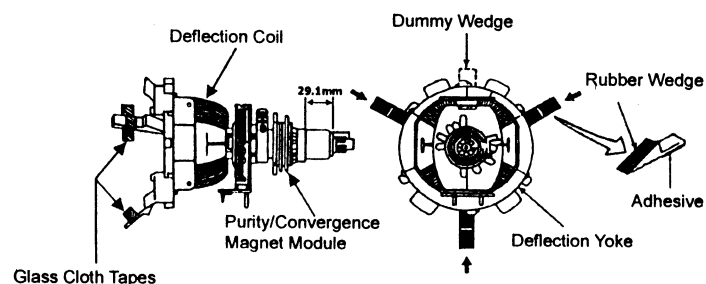


Fig. 1

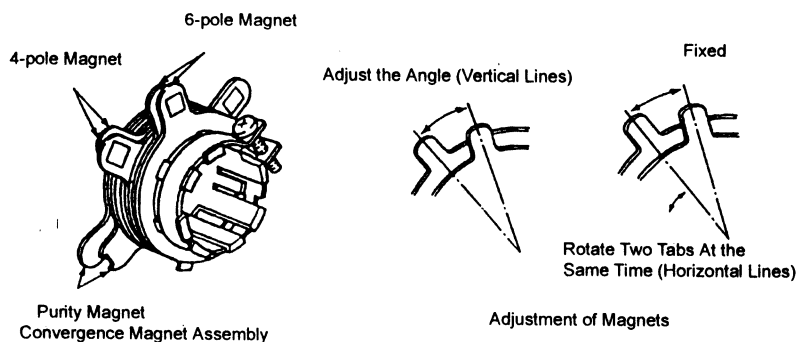


Fig. 2

2. Convergence Adjustment

Preparation:

Before attempting any convergence adjustment, the TV should be operated for at least 15 minutes.

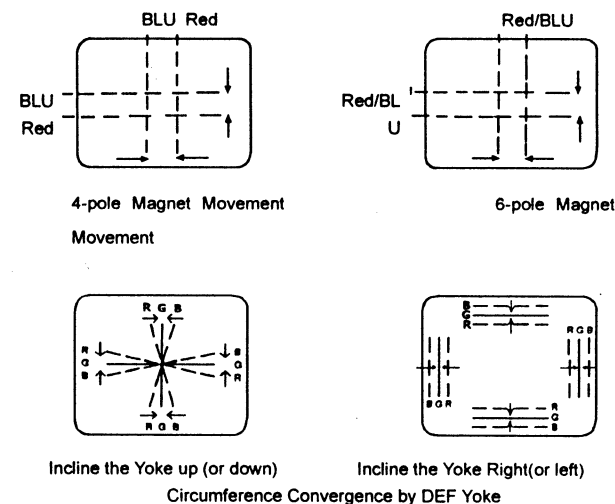
2.1 Center convergence adjustment

- 2.1.1 Receive dot pattern.
- 2.1.2 Adjust the brightness/contrast controls to obtain a sharp picture.
- 2.1.3 Adjust two tabs of the 4-pole magnet to change the angle between them and red and blue vertical lines are superimposed each other on the center of the screen.
- 2.1.4 Turn both tabs at the same time keeping the angle constant to superimpose red and blue horizontal on the center of the screen.
- 2.1.5 Adjust two tabs of the 6-pole magnet to superimpose red/blue line and green line.
- 2.1.6 Remember red and blue movement. Repeat steps 2.1.3~2.1.5 until optimal convergence is

obtained.

2.2 Circumference convergence adjustment

- 2.2.1 Loosen the clamp screw holding the deflection yoke assembly and allow it tilting.
- 2.2.2 Temporarily put the first wedge between the picture tube and deflection yoke assembly. Move front of the deflection yoke up or down to obtain better convergence in circumference. Push the mounted wedge in to fix the yoke temporarily.
- 2.2.3 Put the second wedge into bottom.
- 2.2.4 Move front of the deflection yoke to the left or right to obtain better convergence in circumference.
- 2.2.5 Fix the deflection yoke position and put the third wedge in either upper space. Fasten the deflection yoke assembly on the picture tube.
- 2.2.6 Detach the temporarily mounted wedge and put it in either upper space. Fasten the deflection yoke assembly on the picture tube.
- 2.2.7 After fastening the three wedges, recheck overall convergence and ensure to get optimal convergence. Tighten the lamp screw holding the deflection yoke assembly.



Incline the Yoke up (or down) Incline the Yoke Right (or left)
Circumference Convergence by DEF Yoke

Fig. 3

3. White Balance Adjustment

Generally, white balance adjustment is made with professional equipment. It's not practical to get good white balance only through manual adjustment. For TVs with i²C bus control, change the bus data to adjust white balance.

CIRCUIT ADJUSTMENTS

Preparation:

Circuit adjustments should be made only after completion of set-up adjustments.

Circuit adjustments can be performed using the adjustable components inside the TV set. For TVs with I²C bus control, first change the bus data.

1. Degaussing

A degaussing coil is built inside the TV set. Each time the TV is powered on, the degaussing coil will automatically degauss the TV. If the TV is magnetized by external strong magnetic field, causing color spot on the screen, use a specific degausser to demagnetize the TV in the following ways. Otherwise, color distortion will be shown on the screen.

- 1.1 Power on the TV set and operate it for at least 15 minutes.
- 1.2 Receive red full-field pattern.
- 1.3 Power on the specific degausser and face it to the TV screen.
- 1.4 Turn on the degausser. Slowly move it around the screen and slowly take it away from the TV.
- 1.5 Repeat the above steps until the TV is degaussed completely.

2. Supply Voltage Adjustment

Caution: +B voltage has close relation to high voltage. To prevent X-ray radiation, set +B voltage to the rated voltage.

- 2.1 Make sure that the supply voltage is within the range of the rated value.
- 2.2 Connect a digital voltmeter to the +B voltage output terminal of the TV set. Power on the TV and set the brightness and sub-brightness to minimum.
- 2.3 Regulate voltage adjustment components on the power PCB to make the voltmeter read 115±1V.

3. High Voltage Inspection

Measure voltages of test points on the main PCB with the digital voltmeter. Measure the CRT high voltage with the high-voltage testing equipment and heater voltage with the high-frequency effective voltmeter. The rated values are shown as below.

Table 1

Test Point	Voltage Value (V)
Positive of C492	185V ± 5V
Filament pin of socket	6.3V ± 0.3V _{RMS}
21" CRT anode	25.5KV ± 1.2KV
Anode of 21" pure flat CRT (including Samsung, BMCC and Toshiba)	27.5KV ± 1.5KV
Anode of other 21" pure flat CRTs	26.5KV ± 1.5KV

Notes:

- (1) Measure voltage of the related isoelectronic point if it's difficult to measure a certain point in Table 1.
- (2) Anode high voltage differs depending on CRTs used, which should be decided by engineers.

4. Focus Adjustment

Caution: Dangerously high voltages are present inside the TV. Extreme caution should be exercised when working on the TV with the back removed.

- 4.1 After removing the back cover, look for the FBT on the main PCB. There should be a FCB on the FBT.
- 4.2 Power on the TV and preheat it for 15 min.
- 4.3 Receive a normal TV signal. Rotate knob of the FCB until you get a sharp picture.

5. Safety Inspection

- 5.1 Inspection for insulation and voltage-resistant

Perform safety test for all naked metal of the TV. Supply high voltage of 3000V AC, 50Hz (limit current of 10mA) between all naked metal and cold ground. Test every point for 3 min. and ensure no arcing and sparking.

- 5.2 Requirements for insulation resistance

Measure resistance between naked metal of the TV and feed end of the power cord to be infinity with a DC-500 high resistance meter and insulation resistance between the naked metal and degaussing coil to be over 20M Ω.

6. DESIGN/SERVICE mode

- 6.1 To enter the USER SERVICE mode

Caution: The user service mode adjustment can be changed only when service personnel adjust the whole set data during servicing. As the control data have dramatic effects on functions and performance of the TV, service personnel should not tell user how to enter the SERVICE mode to avoid improper data settings.

- 6.1.1 Set the volume to 0. Then press and hold the MUTE button on the remote control, and press the MENU button on the TV to enter the SERVICE mode. (In this case, the S mode cannot be stored in the EEPROM. To exit from the S mode, turn off the TV set.)
- 6.1.2 After entering the S mode, Red "S" is displayed on the upper center of the screen and MENU1 is default. Use the POS+/- buttons to highlight an adjustment and the VOL+/- buttons to adjust it. The adjusted data are immediately output and stored in the EEPROM

S
 VS 0-3F 25
 xxxxxxxx

6.2 Bus data in the S mode

Table 2 Bus Data

Item	Bus Data	Description	Remarks
5PAR/6PAR	1F	Parallelogram correction (for large-screen only)	
5BOW/6BOW	1F	Curve correction (for large-screen only)	
5HSH/6HSH	Set to the optimal mode	Horizontal center in the TV mode for 50Hz/60Hz For 50Hz, "5HSH" is displayed; for 60Hz, "6HSH" is displayed.	*
5HSR/6HSR	Set to the optimal mode	Horizontal center in the RGB mode for 50Hz/60Hz For 50Hz, "5HSR" is displayed; for 60Hz, "6HSR" is displayed.	*

(continued)

SERVICE MANUAL

5EWP/6EWP	1F	East-West parabola correction (for large-screen only)	
5EWW/6EWW	1F	East-West correction (for large-screen only)	
5UCR/6UCR	1F	Upper corner parabola correction (for large-screen only)	
5LCR/6LCR	1F	Lower corner parabola correction (for large-screen only)	
5EWT/6EWT	1F	Trapezoidal correction (for large-screen only)	
5VSL/6VSL	1F	Vertical slope (for large-screen only)	*
5VAM/6VAM	1F	Vertical amplitude; For 50Hz, "5VAM" is displayed; for 60Hz, "6VAM" is displayed.	*
5SCL/6SCL	Set to the optimal mode	S correction	*
5VSH/6VSH	Set to the optimal mode	Vertical center	*
5VOF/6VOF	Set to the optimal mode	OSD vertical center	*
VX	19	Vertical zoom (for large-screen only)	
RED	20	Red gun cutoff voltage	*
GRN	20	Green gun cutoff voltage	*
WPR	1F	Red gun drive voltage	*
WPG	1F	Green gun drive voltage	*
WPB	1F	Blue gun drive voltage	*
YDFP	07	PAL brightness delay time	
YDFN	07	NTSC brightness delay time	
YDFS	0F	SECAM brightness delay time	
YDAV	0F	AV brightness delay time	
TOP	18	UOC AGC	*
VOL	21	UOC audio output amplitude	*
IFFS	03 (02)	PIF (02-38.9MHz, 03-38MHz)	
HDOL	00	Cathode drive level	
AGC	03	IF ACG speed	
VG2B	3A	VG2 brightness	*
SBRI	1F	Sub brightness	
MBRI	39	Max. brightness	
SCON	20	Sub contrast	
MCON	3F	Max. contrast	
SCOL	32	Sub color	
OP1	BF	Option set byte 1	
OP2	01	Option set byte 2	
OP3	FF	Option set byte 3	
OP4	F6	Option set byte 4	
OP5	76	Option set byte 5	

(continued)

SERVICE MANUAL

OP6	1C (For PF21C99 only) 3C (For PF21C18/21C99E only)	Option set byte 6	
INIT		EEPROM initialization	
VG2		Adjusting screen voltage with VG2	
VSD		Vertical output off	
USER_LOGO		User logo write-in (valid when OP-USER-LOGO is 1)	
STS0/1/2		System status byte	

Notes:

①

SERVICE MANUAL

	5	OP_USER_LOGO	User logo (prior to CHANGHONG logo)
	6	OP_ON_BACK	Auto test background options when power-on: 0-black; 1-blue
	7	OP_FSL	Slicing level for vertical sync
OP3	0	OP_ENGLISH	English
	1	OP_FARSI	Farsi
	2	OP_ARABIC	Arabic
	3	OP_RUSSIAN	Russian
	4	OP_FRENCH	French
	5	OP_GERMAN	German
	6	OP_INDIA	Indonesian
	7	OP_MALAYSIA	Malay
OP4	0	OP_FMWS	Window selection of sound pll: small/large window
	1	OP_DIRECT_SWITCH_ON	Memory power-on (If turned off by the remote control, then the TV is turned on by the remote control; if turned off by the MAIN POWER SWITCH, then turned on by the MAIN POWER SWITCH.)
	2	OP_HCO	EHT tracking mode
	3	OP_CHH_LOGO	User logo display : 1-Displayed without signal reception; 0-No
	4	OP_SOUND_DK	Sound system-DK option set
	5	OP_SOUND_BG	Sound system-BG option set
	6	OP_SOUND_I	Sound system-I option set
	7	OP_SOUND_M	Sound system-M option set
OP5	0	OP_TUNER	Tuner: 1-Philips Tuner 0-Panasonic Tuner
	1	OP_AUTO_LANG0	Auto language option set: English - Farsi - Arabic - Russian - French - German - Indonesian - Malay
	2	OP_AUTO_LANG1	
	3	OP_AUTO_LANG2	
	4	OP_FORF	Field frequency options (OP_FORF/FORF): 00-Auto60Hz, 01-KeepLast, 10-Force60Hz, 11-Auto50Hz
	5	OP_FORF	
	6	OP_AVON	If AV off, then AV on
	7	OP_ONPOSITION	With HOTEL mode preset, on position is fixed to POS1.

(continued)

SERVICE MANUAL

OP6	0	OP_AUTOTEST	Auto test when power-on
	1	OP_PSNS	Sensitivity
	2	OP_BSCREEN	Black screen when changing channels: 1-yes: 0-No
	3	OP_SECAM	1: SECAM option
	4	OP_DFL	Disable flash protection
	5	OP_SIF	External input for sound IF circuit
	6	OP_EXT_SIF0	Sound system options for external circuit: 00-DK, 01-BG,
	7	OP_EXT_SIF1	10-I, 11-M (Valid when OP_SIF = 1)

STRUCTURE AND CHASSIS FUNCTION DESCRIPTION

1. STRUCTURE BLOCK DIAGRAM

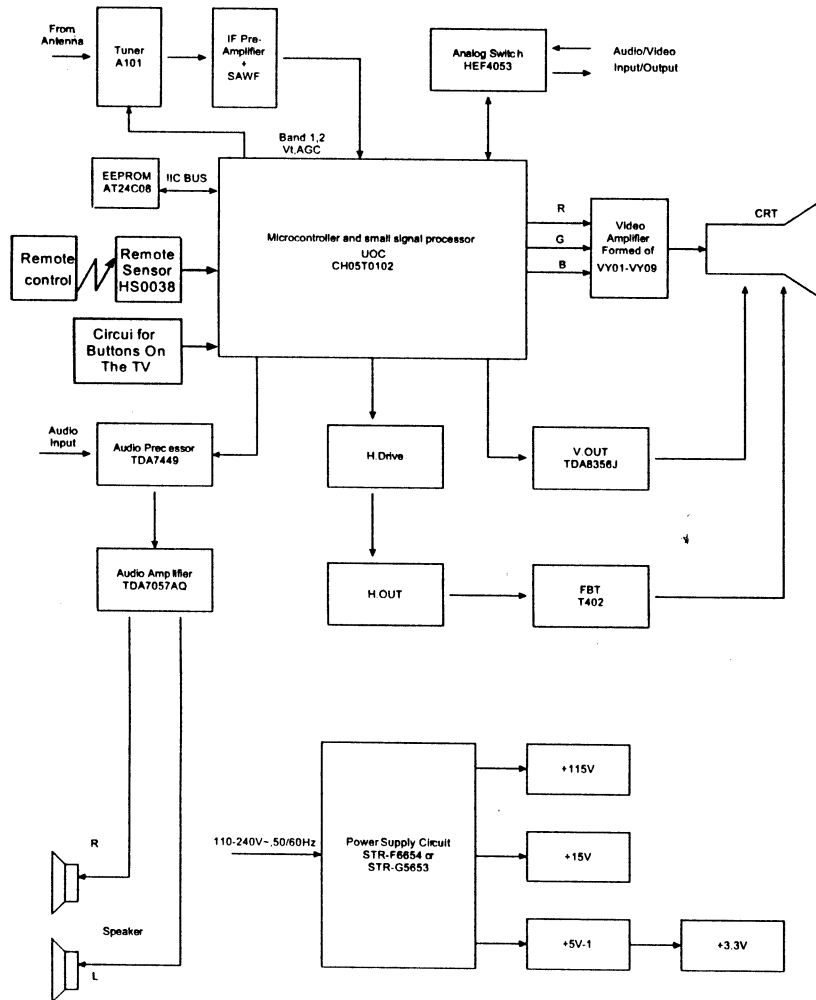


Fig.4 Structure Block Diagram for EX-1A1/1A2 Chassis Series

2. BLOCK DIAGRAM FOR SUPPLY VOLTAGE SYSTEM

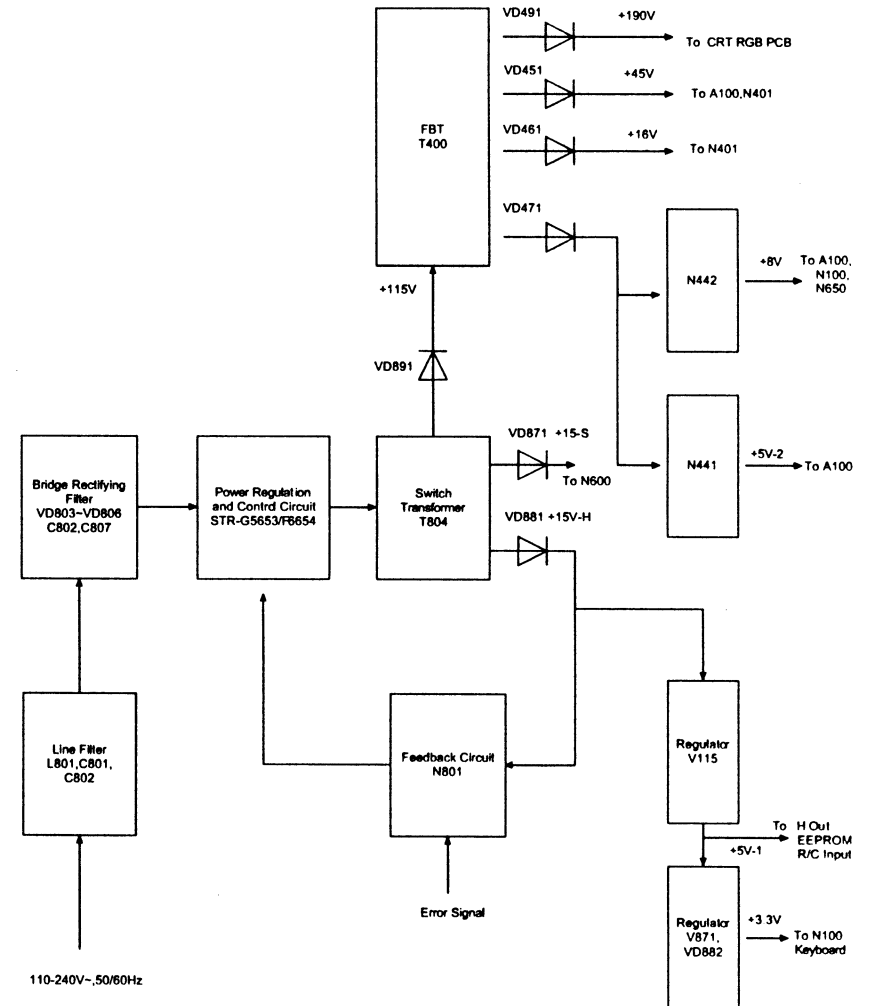


Fig.5 Block Diagram for EX-1A1/1A2 Supply Voltage System

3. CHASSIS DESCRIPTION

1). General Description

EX-1A1 chassis series are applied in 21C19/PF21C19 respectively which uses mainly Philips' advanced UOC-ultimate chip TDA935X/6X/8X and I²C-bus controlled IC. With combination of microcontroller and small signal processor, the TDA935X/6X/8X series feature high-integration, high-performance-to-price ratio and high-reliability and advanced functions with fewer external components, which provide much convenience for manufacturing and technical service.

2). The EX-1A1 chassis series mainly use the following ICs and assemblies.

Table 4 Key ICs and Assemblies

Serial No.	Position	Type	Function Description
1	N100	CH05T0102 (For PF21C18/21C99E) CH05T0101 (For 21C99E)	Microcontroller and small signal processor(UOC)
2	N200	AT24C08	EEPROM
3	N650	TDA7449	Audio processor
4	N401	TDA8356/N6	Vertical scan output stage circuit
5	N600	TDA7057AQ	Sound power amplifier
6	N861	STR-G5653/F6654	Power supply circuit
7	N402	HEF4053	Analog switch
8	A100	TDQ-5B6M	Tuner

SERVICE DATA

1. KEY ICS TECHNICAL DATA

1.1 Microcontroller and small signal processor CH05T0101/CH05T0102 (TDA935X/6X/8X)

The super chips TDA935X/6X/8X are good in pins compatibility. Differences among them are shown as follows.

TDA9351 (48K)	PAL/NTSC/SECAM+1 PAGE TELETEST
TDA9350 (48K)	PAL/NTSC+1 PAGE TELETEST
TDA9361 (64K)	PAL/NTSC/SECAM+10 PAGE TELETEST
TDA9360 (64K)	PAL/NTSC+10 PAGE TELETEST
TDA9380 (32K)	PAL/NTSC
TDA9387 (32K)	NTSC

TDA935X/6X/8X PS/N2 series TV signal processor-Teletext decoder with embedded -Controller

1) General Description

The various versions of the TDA935X/6X/8X PS/N2 series combine the functions of a TV signal processor together with a -Controller and US Closed Caption decoder. Most versions have a Teletext decoder on board. The Teletext decoder has an internal RAM memory for 1 or 10 page text. The ICs are intended to be used in economy television receivers with 90° and 110° picture tubes.

The ICs have supply voltages of 8 V and 3.3 V and they are mounted in S-DIP envelope with 64 pins.

The features are given in the following feature list. The differences between the various ICs are given in the table on page 4.

2) Features

TV-signal processor

- Multi-standard vision IF circuit with alignment-free PLL demodulator
- Internal (switchable) time-constant for the IF-AGC circuit
- A choice can be made between versions with mono intercarrier sound FM demodulator and versions with QSS IF amplifier.
- The mono intercarrier sound versions have a selective FM-PLL demodulator which can be switched to the different FM sound frequencies (4.5/5.5/6.0/6.5 MHz).
The quality of this system is such that the external band-pass filters can be omitted.
- Source selection between 'internal' CVBS and external CVBS or Y/C signals
- Integrated chrominance trap circuit
- Integrated luminance delay line with adjustable delay time
- Picture improvement features with peaking (with variable centre frequency and positive/negative overshoot ratio) and black stretching
- Integrated chroma band-pass filter with switchable centre frequency
- Only one reference (12 MHz) crystal required for the
- -Controller, Teletext- and the colour decoder

- **-Controller**
- **80C51** □ **controller core standard instruction set and timing**
- **1** □ **s machine cycle**
- **16 - 128Kx8-bit late programmed ROM**
- **3 - 12Kx8-bit DATA RAM (shared between Display, Acquisition and Auxiliary Ram)**
- **Interrupt controller for individual enable/disable with two level priority**
- **Two 16-bit Timer/Counter registers**
- **One 16 bit Timer with 8-bit Pre-scaler**
- **WatchDog timer**
- **Auxiliary RAM page pointer**
- **16-bit Data pointer**
- **Stand-by, Idle and Power Down (PD) mode**
- **14 bits PWM for Voltage Synthesis Tuning**
- **8-bit A/D converter**
- **4 pins which can be programmed as general I/O pin, ADC input or PWM (6-bit) output**

- Text memory for 0, 1 or 10 pages
- In the 10 page versions inventory of transmitted Teletext pages stored in the Transmitted Page Table (TPT) and Subtitle Page Table (SPT)
- Data Capture for US Closed Caption
- Data Capture for 525/625 line WST, VPS (PDC system A) and Wide Screen Signalling (WSS) bit decoding
- Automatic selection between 525 WST/625 WST
- Automatic selection between 625 WST/VPS on line 16 of VBI
- Real-time capture and decoding for WST Teletext in Hardware, to enable optimized 11-processor throughput
- Automatic detection of FASTEXT transmission

- Real-time packet 26 engine in Hardware for processing accented, G2 and G3 characters
- Signal quality detector for video and WST/VPS data types
- Comprehensive teletext language coverage
- Full Field and Vertical Blanking Interval (VBI) data capture of WST data

- Teletext and Enhanced OSD modes
- Features of level 1.5 WST and US Close Caption
- Serial and Parallel Display Attributes
- Single/Double/Quadruple Width and Height for characters
- Scrolling of display region
- Variable flash rate controlled by software
- Enhanced display features including overlining, underlining and italics
- Soft colours using CLUT with 4096 colour palette
- Globally selectable scan lines per row (9/10/13/16) and character matrix [12x10, 12x13, 12x16 (VxH)]
- Fringing (Shadow) selectable from N-S-E-W direction
- Fringe colour selectable
- Meshing of defined area
- Contrast reduction of defined area
- Cursor
- Special Graphics Characters with two planes, allowing four colours per character
- 32 software redefinable On-Screen display characters
- 4 WST Character sets (G0/G2) in single device (e.g. Latin, Cyrillic, Greek, Arabic)
- G1 Mosaic graphics, Limited G3 Line drawing characters
- WST Character sets and Closed Caption Character set in single device

**Ic Version (TDA) 9350 9351 9352 9353 9360 9361 9362 9363 9364 9365 9366 9367 9380 9381 9382
9383 9384 9385 9386 9387 9388 9389**

IC VERSION(TDA)	3050	3051	3052	3053	3060	3061	3062	3063	3064	3065	3066	3067	3080	3081	3082	3083	3084	3085	3086	3087	3088	3089
TV range	90°	90°	90°	110°	90°	90°	110°	110°	110°	110°	90°	90°	90°	90°	90°	110°	110°	110°	110°	90°	110°	110°
Mono intercarrier multi standard sound demodulator (4.5-6.5 MHz) with switchable centre frequency	✓	✓			✓	✓	✓						✓			✓	✓			✓	✓	
Audio switch	✓	✓			✓	✓	✓	✓					✓			✓	✓				✓	✓
Automatic Volume Levelling	✓	✓	✓	✓	✓	✓	✓						✓	✓	✓						✓	✓
Automatic Volume Levelling or subcarrier output (for combfilter applications)							✓		✓	✓						✓	✓		✓	✓	✓	✓
Des sound IF amplifier with separate input and AGC circuit				✓											✓				✓			
AM sound demodulator without extra reference circuit										✓									✓			
PAL decoder	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
SECAM decoder	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
NTSC decoder	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
Horizontal geometry (E-W)	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
Horizontal and Vertical Zoom				✓			✓	✓	✓	✓	✓					✓	✓	✓	✓	✓	✓	✓
ROM size	32-64K	32-64K	32-64K	32-64K	64-128K	64-128K	64-128K	64-128K	64-128K	64-128K	64-128K	64-128K	16-64K	16-64K	16-64K	16-64K	16-64K	16-64K	16-64K	16-64K	16-64K	16-64K
User RAM size	1K	1K	1K	1K	2K	2K	2K	2K	2K	2K	2K	2K	1K	1K	1K	1K	1K	1K	1K	1K	1K	1K
Teletext	page/page	page/page	page/page	page/page	10 page/page	10 page/page	10 page/page	10 page/page	10 page/page	10 page/page	10 page/page	10 page/page	page/page	page/page	page/page	page/page	page/page	page/page	page/page	page/page	page/page	page/page
Closed captioning																✓	✓	✓	✓	✓	✓	✓

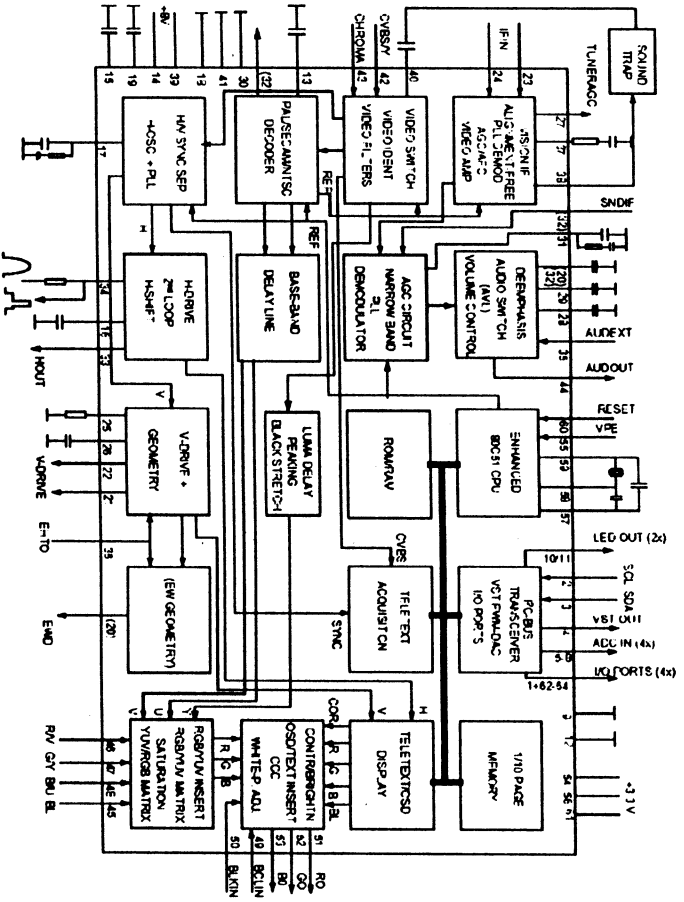


Fig.6 Block Diagram for TDA955x/6x8xP/SN2 with Mono Intercarrier Sound Demodulator

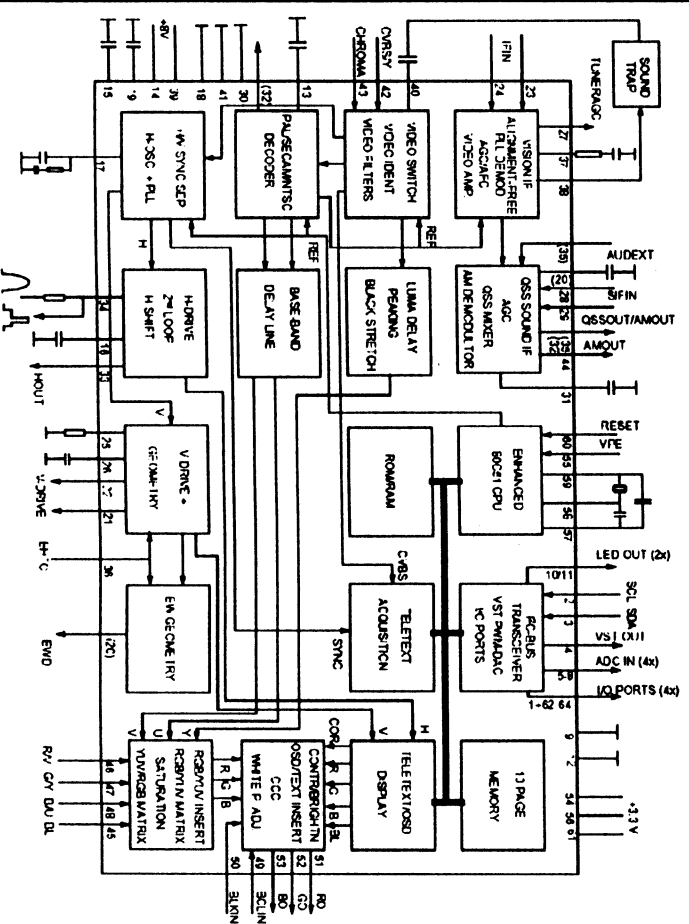


Fig. 7 Block Diagram for TDA935* $\frac{1}{6}$ *8*P/S/N2 with QSS IF Sound Channel

4) pinning

Table 5

SYMBOL	PIN	DESCRIPTION
P1.3/T1	1	port 1.3 or Counter/Timer 1 input
P1.6/SCL	2	port 1.6 or I ² C-bus clock line
P1.7/SDA	3	port 1.7 or I ² C-bus data line
P2.0/TPWM	4	port 2.0 or Tuning PWM output
P3.0/ADCO/PWMO	5	port 3.0 or ADC0 input or PWM0 output
P3.1/ADC1/PWM1	6	port 3.1 or ADC1 input or PWM1 output
P3.2/ADC1/PWM2	7	port 3.2 or ADC2 input or PWM2 output
P3.3/ADC3/PWM3	8	port 3.3 or ADC3 input or PWM3 output
VSSC/P	9	digital ground for μ -Controller core and periphery
P0.5	10	port 0.5 (8mA current sinking capability for direct drive of LEDs)
P0.6	11	port 0.6 (8mA current sinking capability for direct drive of LEDs)
VSSA	12	analog ground of Teletext decoder and digital ground of TV-processor
SECPLL	13	SECAM PLL decoupling
VP2	14	2nd supply voltage TV-processor (+8V)
DECDIG	15	decoupling digital supply of TV-processor
PH2LF	16	phase-2 filter
PH1LF	17	phase-1 filter
GND3	18	ground 3 for TV-processor
DECBG	19	bandgap decoupling
AVL/EWD ⁽¹⁾	20	Automatic Volume Levelling/East-West drive output
VDRB	21	vertical drive B output
VDRA	22	vertical drive A output
IFIN1	23	IF input 1
IFIN2	24	IF input 2
IREF	25	reference current input
VSC	26	vertical sawtooth capacitor
TUNERAGC	27	tuner AGC output
AUDEEM/SIFIN1 ⁽¹⁾	28	audio deemphasis or SIF input 1
DECSDEM/SIFIN2 ⁽¹⁾	29	decoupling sound demodulator or SIF input2
GND2	30	ground 2 for TV processor
SNDPLL/SIFAGC ⁽¹⁾	31	narrow band PLL filter/AGC sound IF
AVL/SNDIF/REFO/ AMOUT ⁽¹⁾	32	Automatic Volume Levelling/sound IF input/subcarrier reference output/AM output
HOUT	33	(non controlled)
FBISO	34	horizontal output
AUDEXT/ QSSO/AMOUT ⁽¹⁾	35	flyback input/sand castle output
EHTO	36	external audio input/QSS intercarrier out/AM audio output (non controlled)
PLLIF	37	EHT/overvoltage protection input
IFVO/SVO	38	IF-PLL loop filter
VP1	39	IF video output/selected CVBS output
CVBSINT	40	main supply voltage TV-processor (+8V)
GND1	41	internal CVBS input
CVBS/Y	42	ground 1 for TV-processor
CHROMA	43	external CVBS/Y input
AUDOUT/AMOUT ⁽¹⁾	44	chrominance input (SVHS)
INSSW2	45	2nd RGB/YUV insertion input
R2/VIN	46	2nd R input/V (R-Y) input
G2/YIN	47	2nd G input/U input

SYMBOL	PIN	DESCRIPTION
B2/UIN	48	2nd B input/U (B-Y) input
BCLIN	49	beam current limiter input/(V-guard input, note2)
BLKIN	50	black current input/(V-guard input, note2)
RO	51	Red output
GO	52	Green output
BO	53	Blue output
VDDA	54	analog supply of Teletext decoder and digital supply of TV-processor (3.3V)
VPE	55	OTP programming Voltage
VDDC	56	digital supply to core (3.3V)
OSCGND	57	oscillator ground supply
XTALIN	58	crystal oscillator input
XTALOUT	59	crystal oscillator output
RESET	60	reset
VDDP	61	digital supply to periphery (+3.3V)
P1.0/INT1	62	port 1.0 or external interrupt 1 input
P1.1/TO	63	port 1.1 or Counter/Timer 0 input
P1.2/INT0	64	port 1.2 or external interrupt 0 input

Notes

- 1)The function of pin 20, 28, 29, 31, 32, 35 and 44 is dependent on the IC version (mono intercarrier FM demodulator/QSS IF amplifier and East-West output or not) and on some software control bits. The valid combinations are given in table 2.
- 2)The vertical guard function can be controlled via pin 49 or pin 50. The selection is made by means of the IVG bit in subaddress 2BH.

Table 6 Pin functions for various versions

IC version	FM-PLL Version				QSS Version					
East-West Y/N	N		Y		N			Y		
CMB1/CM BO bits	00	01/10/11	00	01/10/11	00	01/10/11		00	01/10/11	
AM bit	-	-	-	-	-	0	1	-	0	1
Pin 20	AVL		EWD		AVL			EWD		
Pin 28	AUDEEM				SIFIN1					
Pin 29	DECSDEM				SIFIN2					
Pin 31	SNDPLL				SIFAGC					
Pin 32	SNDIR ⁽¹⁾	REFO ⁽²⁾	AVL/SNDIF ⁽¹⁾	REFO ⁽²⁾	AMOUT	REFO ⁽²⁾		AMOUT	REFO ⁽²⁾	
Pin 35	AUDEXT				AUDEXT	QSSO	AMOUT	AUDEXT	QSSO	AMOUT
Pin44	AUDOUT				Controlled AM or audio out					

Notes

- 1)When additional(external) selectivity is required for FM-PLL system pin 32 can be used as sound IF input. This function is selected by means of SIF bit in subaddress 28H.
- 2)The reference output signal is only available for the CMB1/CMBO setting of 0/1. For the other settings this pin is a switch output.

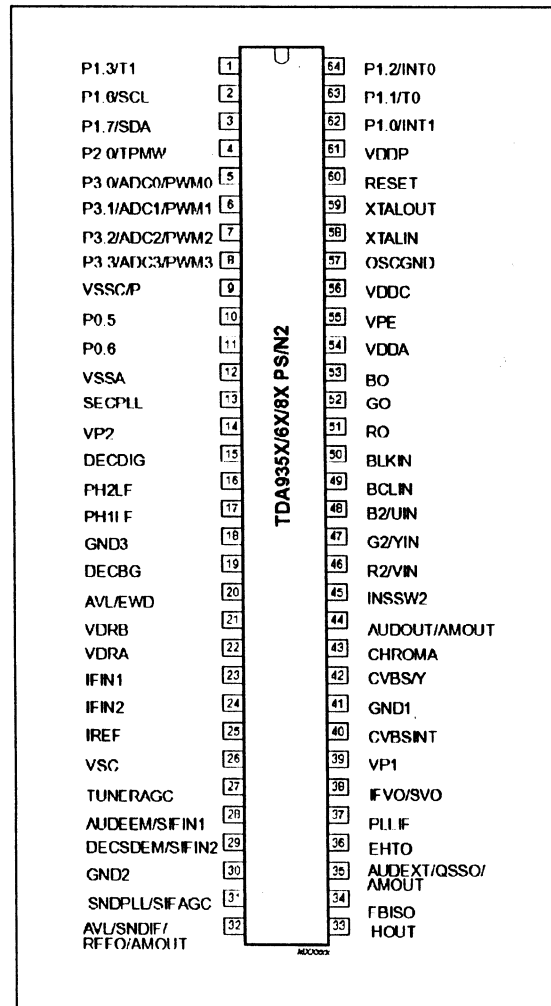


Fig.8 Pin Configuration (SDIP 64)

1.2 Electronic switch circuit HEF4053

Triple 2-channel Analog

Multiplexer/Demultiplexer

1). Description

The HEF4053 is a triple 2-channel analog multiplexer/demultiplexer with a common enable input (\bar{E}). Each multiplexer/demultiplexer has two independent inputs/outputs (Y_0 and Y_1), a common input/output (Z), and select inputs (S_n). Each also contains two-bidirectional analog switches, each with one side connected to an independent input/output (Y_0 and Y_1) and the other side connected to a common input/output (Z). With (\bar{E}) LOW, one of the two switches is selected

(low impedance ON-state) by S_n . With \bar{E} HIGH, all switches are in the high impedance OFF-state, independent of S_A to S_C . V_{DD} and V_{SS} are the supply voltage connections for the digital control inputs (S_A to S_C and \bar{E}). The V_{DD} to V_{SS} range is 3 to 15V. The analog inputs/outputs (Y_0 , Y_1 and Z) can swing between V_{DD} as a positive limit and V_{EE} as a negative limit. $V_{DD}-V_{EE}$ may not exceed 15 V. For operation as a digital multiplexer/demultiplexer, V_{EE} is connected to V_{SS} (typically ground).

2). Block Diagrams

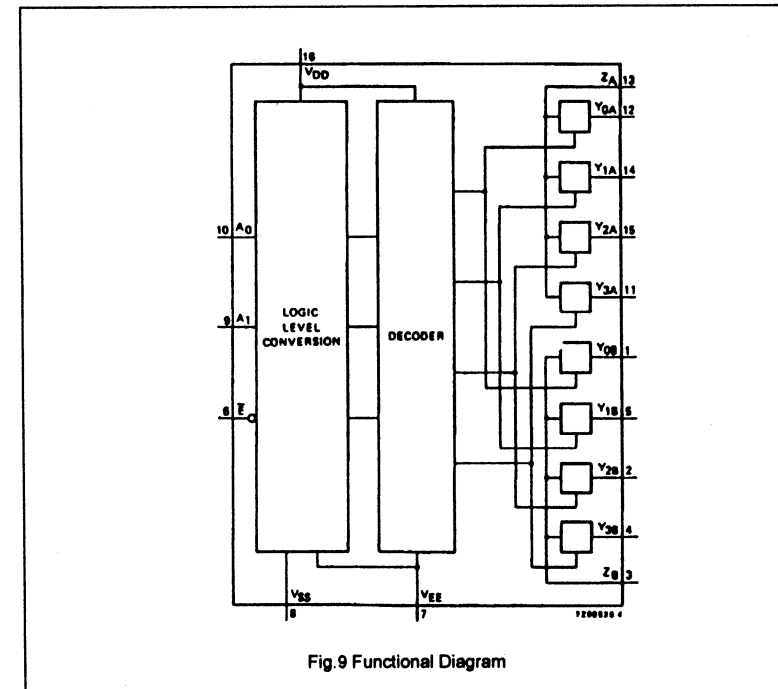


Fig.9 Functional Diagram

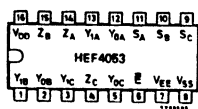


Fig. 10 Pinning Diagram

HEF4053P(N): 16-lead DIL; plastic
(SOT38-1)

HEF4053D(F): 16-lead DIL; ceramic
(cerdip)
(SOT74)

HEF4053T(D): 16-lead S0; plastic
(SOT109-1)

(:): Package Designator North America

Pinning

Y_{0A} to Y_{0C}	Independent inputs/outputs
Y_{1A} to Y_{1C}	Independent inputs/outputs
S_A to S_C	Select inputs
E	Enable input (active LOW)
Z_A to Z_C	Common inputs/outputs

3. Function Table

Inputs		Channel
E	S_n	On
L	L	Y_{0n} - Z_n
L	H	Y_{1n} - Z_n
H	X	none

Notes

H=HIGH state (the more positive voltage)

L=LOW state (the less positive voltage)

X=STATE is immaterial

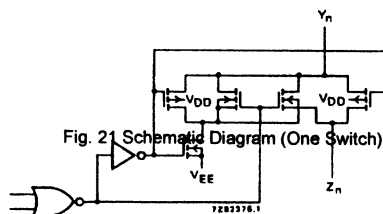


Fig 11

Ratings

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Supply voltage (with reference to V_{DD}) V_{EE} -18 to +0,5 V

Note

To avoid drawing V_{DD} current out of terminal Z, when switch current flows into terminals Y, the voltage drop across the bidirectional switch must not exceed 0,4 V. If the switch current flows into terminal Z, no V_{DD} current will flow out of terminals Y, in this case there is no limit for the voltage drop across the switch, but the voltages at Y and Z may not exceed V_{DD} or V_{EE} .

1.3 Sound power amplifier TDA7057AQ**2x8W Stereo BTL Audio Output Amplifier with DC Volume Control****1). Features**

- DC volume control
- Few external components
- Mute mode
- Thermal protection
- Short-circuit proof
- No switch-on and switch-off clicks
- Good overall stability
- Low power consumption
- Low HF radiation
- ESD protected on all pins.

2). General Description

The TDA7057AQ is a stereo BTL output amplifier with DC volume control. The device is designed for use in TVs and monitors, but is also suitable for battery-fed portable recorders and radios.

Missing Current Limiter (MCL)

A MCL protection circuit is built-in. The MCL circuit is activated when the difference in current between the output terminal of each amplifier exceeds 100 mA (typical 300 Ma). This level of 100 mA allows for single-ended headphone applications.

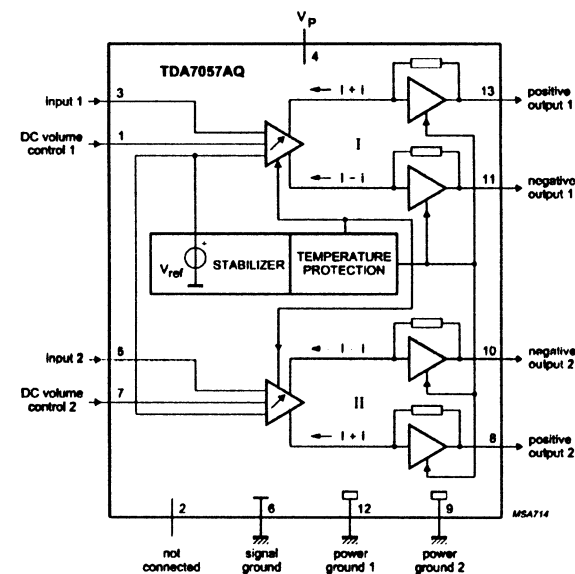
3). Block Diagram

Fig.12

1.4 Vertical scan output stage circuit TDA8356/N6**DC-coupled vertical deflection circuit TDA8356****1) Features**

- Few external components
- Highly efficient fully DC-coupled vertical output bridge circuit
- Vertical flyback switch
- Guard circuit
- Protection against:
 - Short-circuit of the output pins (7 and 4)
 - Short-circuit of the output pins to VP.
- Temperature protection
- High EMC immunity because of common mode inputs
- A guard signal in zoom mode.

2) General Description

The TDA8356 is a power circuit for use in 90, and 110, colour deflection systems for field frequencies of 50 to 120 Hz. The circuit provides a DC driven vertical deflection output circuit, operating as a highly efficient class G system.

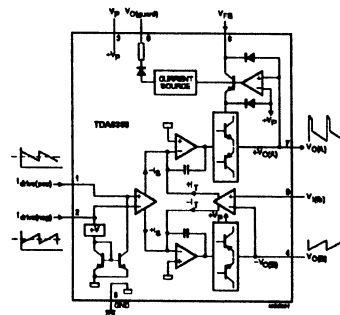
3) Block Diagram

Fig.13

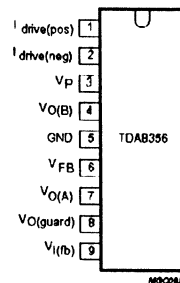
4) Pinning

Fig.14

Table 7 Pinning Description

SYMBOL	PIN	DESCRIPTION
I drive(pos)	1	input power-stage (positive); includes I1(sb) signal bias
I drive(neg)	2	input power-stage (negative); includes I1(sb) signal bias
VP	3	operating supply voltage
VO(B)	4	output voltage B
GND	5	ground
VFB	6	input flyback supply voltage
VO(A)	7	output voltage A
VO(guard)	8	guard output voltage
VI(fb)	9	input feedback voltage

1.5 Audio effect processor TDA7449**TDA7449 TONE CONTROL DIGITALLY CONTROLLED AUDIO PROCESSOR INPUT MULTIPLEXER****1) Features**

- 2 stereo inputs
- selectable input gain for optimal Adaptation to different sources One stereo output Treble, and bass control in 2.0db Steps Volume control in 1.0db steps Two speaker attenuators:
- two independent speaker control In 1.0db steps for balance facility
- independent mute function All function are programmable via Serial bus



DIP20

2) description

The tda7449 is a volume tone (bass and treble) Balance (left/right) processor for quality audio Applications in TV systems.

Selectable input gain is provided. Control of all The functions is accomplished by serial bus. The ac signal setting is obtained by resistor networks And switches combined with operational Amplifiers.

Thanks to the used bipolar/cmotechnology, Low distortion, low noise and dc stepping are Obtained.

ORDERING NUMBER: TDA7449

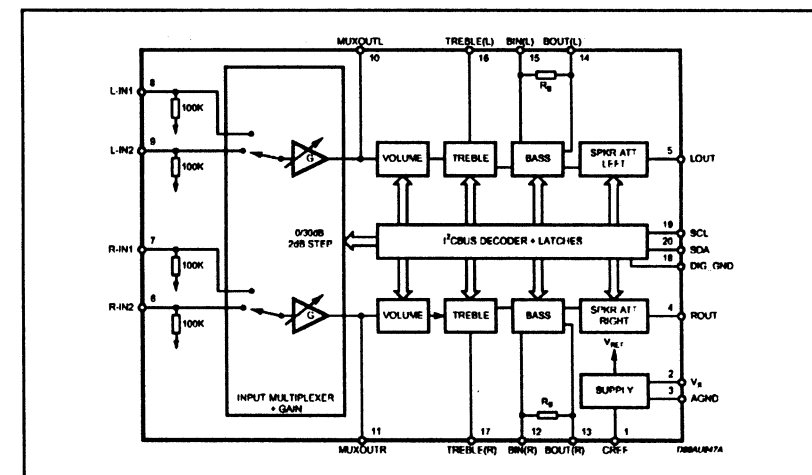
3) Block Diagram

Fig.15

4) Pin Connection

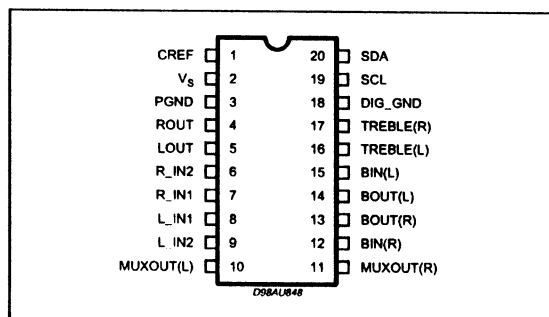


Fig. 16

5) Electrical Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
AUDIO OUTPUTS						
V_{CLIP}	Clipping Level	$d=0.3\%$	2.1	2.6		V _{rms}
R_L	Output Load Resistance		2			K Ω
R_o	Output Impedance		10	4.0	70	Ω
V_{OC}	DC Voltage Level			3.8		V
GENERAL						
E_{NO}	Output Noise	All gains=0dB; BW=20Hz to 20KHz flat		5	15	μ V
E_t	Total Tracking Error	$A_v=0$ to -24dB $A_v=-24$ to -47dB		0	1	dB
S/N	Signal to Noise Ratio	All gains 0dB; $V_o=1V_{RMS}$		106		dB
S_c	Channel Separation Left/Right		80	100		dB
d	Distortion	$A_v=0$; $V_1=1V_{RMS}$		0.01	0.08	%
BUS INPUT						
V_L	Input Low Voltage				1	V
V_H	Input High Voltage		3			V
I_{IN}	Input Current	$V_{IN}=0.4V$	-5		5	μ V
V_o	Output Voltage SDA Acknowledge	$I_o=1.6mA$		0.4	0.8	V

ELECTRICAL CHARACTERISTICS (refer to the test circuit $T_{amb} = 25^\circ C$, $V_S = 9V$, $R_L = 10K \Omega$, $R_G = 600 \Omega$, all controls flat ($G = 0dB$), unless otherwise specified).

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
SUPPLY						
V_S	Supply Voltage		6	9	10.2	V
I_S	Supply Current			7		mA
SVR	Ripple Rejection		60	90		dB
INPUT STAGE						
R_{IN}	Input Resistance			100		K Ω
V_{CL}	Clipping Level	THD = 0.3%	2	2.5		V _{rms}
S_{IN}	Input Separation	The selected input is grounded through a 2.2mF capacitor	80	100		dB
G_{inmin}	Minimum Input Gain		-1	0	1	dB
G_{inmax}	Maximum Input Gain			30		dB
G_{step}	Step Resolution			2		dB
VOLUME CONTROL						
CRANGE	Control Range		45	47	49	dB
AVMAX	Max. Attenuation		45	47	49	dB
ASTEP	Step Resolution		0.5	1	1.5	dB
EA	Attenuation Set Error	$A_v = 0$ to -24dB	-1.0	0	1.0	dB
		$A_v = -24$ to -47dB	-1.5	0	1.5	dB
ET	Tracking Error	$A_v = 0$ to -24dB		0	1	dB
		$A_v = -24$ to -47dB		0	2	dB
VDC	DC Step	adjacent attenuation steps from 0dB to AV max		0.5	3	mv
Amute	Mute Attenuation		80	100		dB
BASS CONTROL (1)						
G_b	Control Range	Max. Boost/cut	± 12.0	± 14.0	± 16.0	dB
BSTEP	Step Resolution		1	2	3	dB
RB	Internal Feedback Resistance		18.75	25	31.25	K Ω
TREBLE CONTROL (1)						
G_t	Control Range	Max. Boost/cut	± 13.0	± 14.0	± 15.0	dB
TSTEP	Step Resolution		1	2	3	dB
SPEAKER ATTENUATORS						
CRANGE	Control Range			76		dB
SSTEP	Step Resolution		0.5	1	1.5	dB
EA	Attenuation Set Error	$A_v = 0$ to -20dB	-1.5	0	1.5	dB
		$A_v = -20$ to -56dB	-2	0	2	dB
VDC	DC Step	adjacent attenuation steps		0	3	mv
Amute	Mute Attenuation		80	100		dB

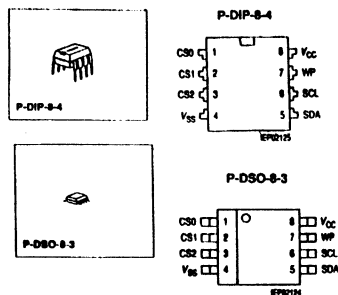
NOTE1:

- 1) The device is functionally good at $V_S = 5V$. a step down, on V_S , to 4V doesn't reset the device.
- 2) BASS and TREBLE response: The center frequency and the response quality can be chosen by the external circuitry.

1.7 Power module STR-G5653/6454R

Switch-mode Power Supply STR- G5653/F6654

- ## 2) Pin Configuration



1) Features

- Flyback Operation with Quasi-Resonant Soft Switching for Low Power Dissipation and EMI
- Rugged Avalanche-Rated MOSFET
- Soft drive circuit MOSFET
- Adjustable MOSFET switching speed
- Choice of MOSFET Voltage and rDS(on)
- Full Over-Current Protection (no blanking)
- Under-Voltage Lockout with Hysteresis
- Over-Voltage Protection
- Direct Voltage Feedback
- Low Start-up Current (100 μ Amax)
- Low-Frequency, Low-Power Standby Operation
- Overmolded 5-Pin Package

2). Circuit Block Diagram

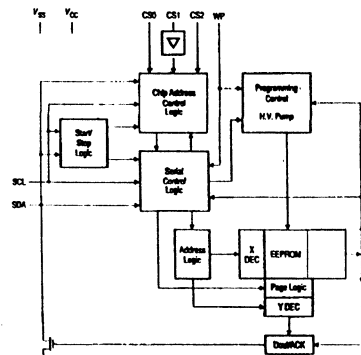


Fig. 17

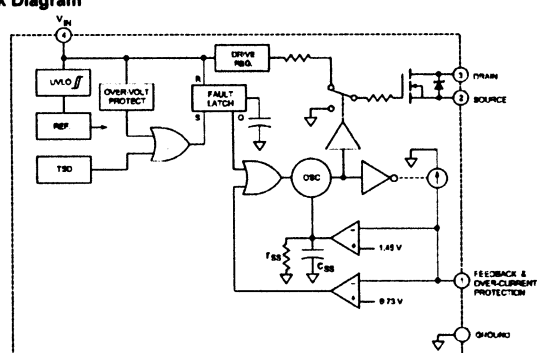


Fig.18

3). Pin Configuration and Functions

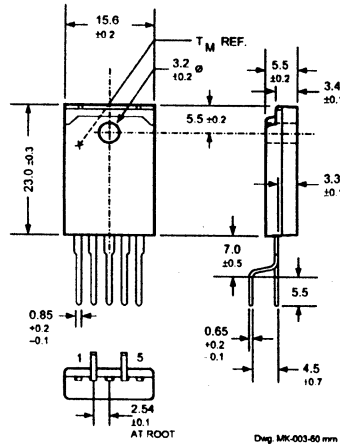


Fig. 19

3.1) Table 8 Pin function for STR-G5653

Pin No.	Symbol	Function Description
1	D	MOSFET drain
2	S	MOSFET source
3	GND	Ground
4	V _{IN}	Supply voltage input for control circuit
5	OCP/FB	Over-current protection detection signal/ voltage-limiting signal input

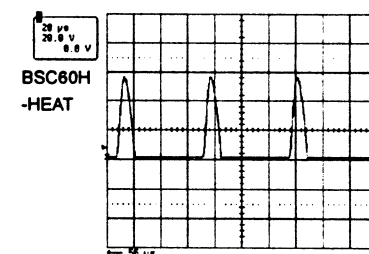
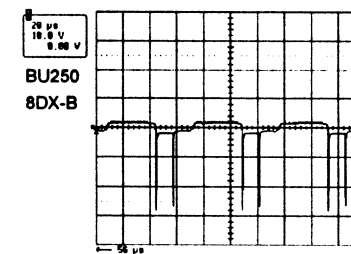
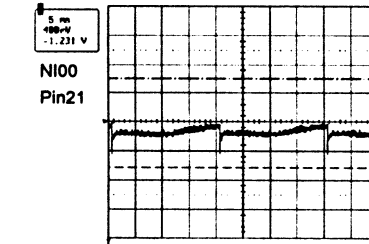
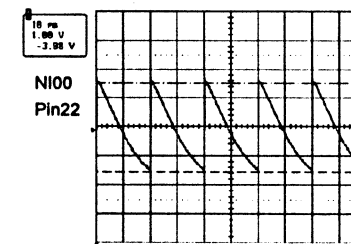
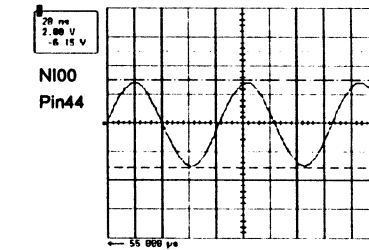
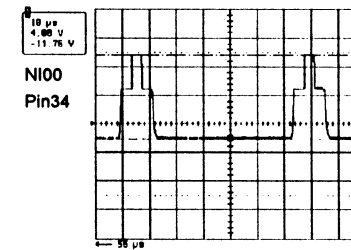
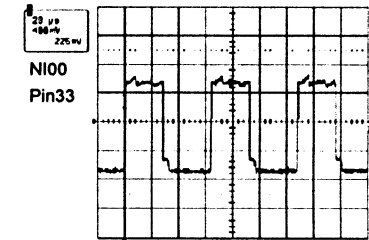
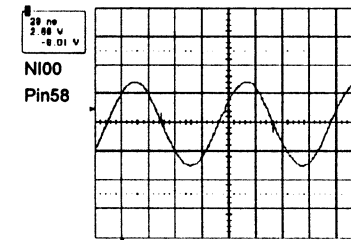
3.2) Table 9 Pin function for STR-F6654

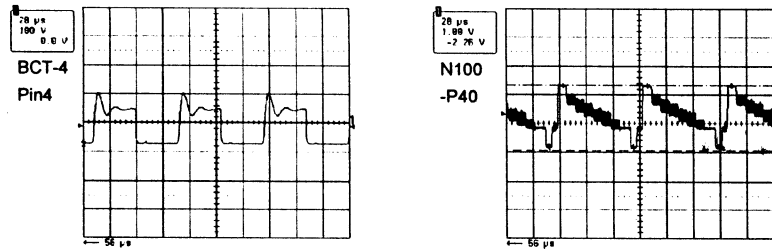
Pin No.	Symbol	Function Description
1	OCP/FB	Over-current protection detection signal/ voltage-limiting signal input
2	S	MOSFET source
3	D	MOSFET drain
4	V _{IN}	Supply voltage input for control circuit
5	GND	Ground

4). Difference between STR-G5653 and STR-F6654

- Different size: STR- F6654 is larger
- Different pin functions
- Different electric characteristics: Larger power output, switching current, avalanche-rated and internal allowable power consumption for STR-F6654
- internal allowable power consumption for STR-F6654

3. WAVEFORMS OF KEY POINTS





Notes:

①

4. KEY ICS SERVICE DATA

Table 10 Function and Service Data of TDA7057AQ (N600)'s Pins

Pin No.	Function description	Digital Multimeter : Victor DT890D		
		Reference Voltage (V)	Positive Resistance (K Ω)	Negative Resistance (K Ω)
1	Vertical drive input (positive)	7.5	22.6	18.6
2	Vertical drive input (negative)	0	0	0.
3	Feedback input	7.5	22.6	18.6
4	Supply voltage	7.5	22.6	18.6
5	Output 1	0	0	0
6	Not connected	7.5	22.6	18.6
7	Ground	0.8	8.7	8.8
8	Pump supply voltage input	0	0	0.
9	Output 1	2.4	54.7	23.6
10	Guard output	16.0	2.4	2.4
11	Pincushion output	2.4	55.1	23.6
12	Pincushion input (negative)	0	-	-
13	Pincushion input (positive)	0.8	9.0	8.8

Table 11 Function and Service Data of TDA8356 (N401)'s Pins

Pin No.	Symbol	Digital Multimeter : Victor DT890D		
		Reference Voltage (V)	Positive Resistance (K Ω)	Negative Resistance (K Ω)
1	Idrive (pos)	2.4	27.7	20.3
2	Idrive (neg)	2.4	27.7	20.4
3	VP	15.4	26.3	13.5
4	VO (B)	7.7	6.1	6.1
5	GND	0	0	0
6	VFB	45.0	113.3	13.7
7	VO (A)	7.5	6.1	6.1
8	VO(guard)	0.2	10.0	9.7
9	VI(fb)	7.7	6.1	6.1

Table 12 Function and Service Data of HEF4053BP (N402)'s Pins

Pin No.	Function description	Digital Multimeter : Victor DT890D		
		Reference Voltage (V)	Positive Resistance (K Ω)	Negative Resistance (K Ω)
1	Signal input	3.0	22.2	47.0
2	Signal input	3.9	22.2	47.0
3	Signal input	1.3	21.2	40.0
4	Signal output	2.5	0	0
5	Signal input	2.4	-	-

(continued)

SERVICE MANUAL

6	Ground	0	0	0
7	Ground	0	0	0
8	Ground	0	0	0.
9	Control signal input	0.03	13.5	13.1
10	Control signal input	0.03	13.5	13.1
11	Control signal input	0.03	13.5	13.1
12	Signal input	3.9	22.2	47.0
13	Signal output	0.7	22.2	47.0
14	Signal input	3.9	21.9	26.0
15	Audio output	3.9	21.9	26.0
16	Supply voltage	7.8	0	0

Table 13 Function and Service Data of TDA7449 (N650)'s Pins

Pin No.	Symbol	Digital Multimeter : Victor DT890D		
		Reference Voltage (V)	Positive Resistance (K Ω)	Negative Resistance (K Ω)
1	CREF	4.0	28.2	21.9
2	VS	8.0	1.8	1.8
3	PGND	0	0	0
4	ROUT	3.3	9.4	9.3
5	LOUT	3.3	9.4	9.3
6	R_IN1	4.0	21.9	26.0
7	R_IN2	4.0	26.7	24.3
8	L_IN1	4.0	26.7	24.3
9	L_IN2	4.0	21.9	26.1
10	MUXOUT(L)	4.0	24.5	22.5
11	MUXOUT(R)	4.0	24.5	22.5
12	BIN(R)	4.0	28.0	26.1
13	BOUR(R)	4.0	28.0	21.0
14	BOUR(L)	4.0	28.0	20.8
15	TREBLE(L)	4.0	28.0	26.1
16	BIN(L)	4.0	30.5	28.0
17	TREBLE(R)	4.0	30.5	28.0
18	DIG_GND	0	0	0
19	SCL	3.4	6.8	6.7
20	SDA	3.1	6.8	6.7

Table 14 Function and Service Data of TDA9351PS/N2/3I (N100)'s Pins

Pin No.	Symbol	Digital Multimeter : Victor DT890D		
		Reference Voltage (V)	Positive Resistance (K Ω)	Negative Resistance (K Ω)
1	P1.3/T1	3.8	9.5	9.5
2	P1.6/SCL	3.4	6.8	6.8

(continued)

SERVICE MANUAL

3	P1.7/SDA	3.0	6.8	6.8
4	P2.0/TPWM	1.8	36.2	17.9
5	P3.0/ADC0/PWM0	0.1	11.8	12.3
6	P3.1/ADC1/PWM1	0.1	5.0	5.0
7	P3.2/ADC2/PWM2	0.02	13.5	13.5
8	P3.3/ADC3/PWM3	0.7	10.7	9.3
9	VSSC/P	0	0	0
10	P0.5	0.01	13.3	13.3
11	P0.6	4.2	11.6	11.0
12	VSSA	0	0	0
13	SECPLL	2.3	27.6	21.7
14	VP2	8.0	1.8	1.8
15	DECDIG	5.0	23.1	16.1
16	PH2LF	3.2	27.5	21.0
17	PH1LF	3.9	27.9	21.2
18	GND3	0	0	0
19	DECBG	4.0	24.4	18.7
20	AVL/EWD (1)	0.01	27.6	21.2
21	VDRA	2.4	27.6	20.4
22	IFIN1	2.4	27.6	20.3
23	IFIN2	1.8	24.7	19.8
24	IREF	1.8	24.7	19.9
25	TUNERAGC	3.8	25.3	20.5
26	AUDEEM/SIFIN1(1)	3.8	27.7	21.5
27	DECSDEM/SIFIN2(1)	1.6	8.4	8.4
28	GND2	3.2	27.1	20.7
29	VSSA	2.3	27.8	21.6
30	SECPLL	0	0	0
31	SNDPLL/SIFAGC(1)	2.3	27.8	21.8
32	AVL/SNDIF/REF0/AMOUT(1)	0.2	27.2	20.4
33	HOUT	0.4	5.3	5.3
34	FBISO	0.5	24.0	18.8
35	AUDEXT/QSSO/AMOUT(1)	3.7	27.8	21.5
36	AUDEXT/	1.6	18.7	16.0
37	QSSO/AMOUT(1)	2.4	27.8	21.5
38	AUDEXT/	3.3	24.0	21.1
39	QSSO/AMOUT(1)	7.7	1.8	1.8
40	AUDEXT/	3.8	27.5	21.2
41	GND1	0	0	0
42	CVBS/Y	3.3	27.5	21.2

(continued)

SERVICE MANUAL

43	CHROMA	1.4	26.8	20.9
44	AUDOUT /AMOUT(1)	3.3	27.5	21.5
45	INSSW2	1.6	1.0	1.0
46	R2/VIN	2.6	28.0	21.7
47	G2/YIN	2.6	28.0	21.7
48	B2/UIN	2.6	28.0	21.7
49	BCLIN	2.5	27.6	20.8
50	BLKIN	6.0	27.9	21.3
51	RO	2.6	1.1	1.1
52	GO	2.5	1.1	1.1
53	BO	2.4	1.1	1.1
54	VDDA	3.2	11.0	11.3
55	VPE	0	0	0
56	VDDC	3.2	11.0	11.3
57	OSCGND	0.02	-	-
58	XTALIN	-	-	-
59	XTALOUT	-	-	-
60	RESET	0	0	0
61	VDDP	3.2	11.0	11.3
62	P1.0/INT1	0	-	-
63	P1.1/T0	1.5	3.3	3.3
64	P1.2/INT0	5.0	18.2	17.5

Notes:

③

SERVICE MANUAL

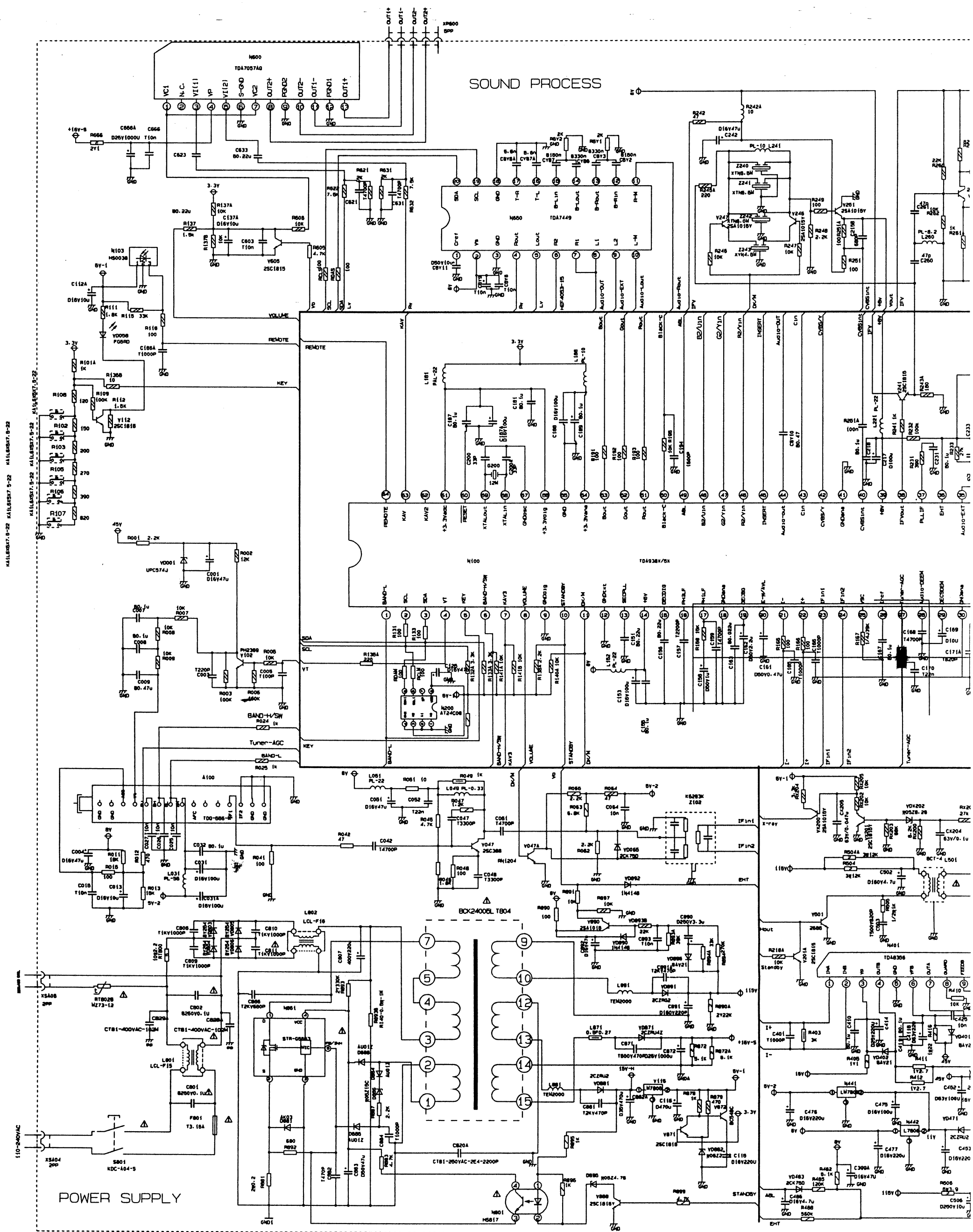
4. REPLACEMENT OF PARTS

4.1 Description

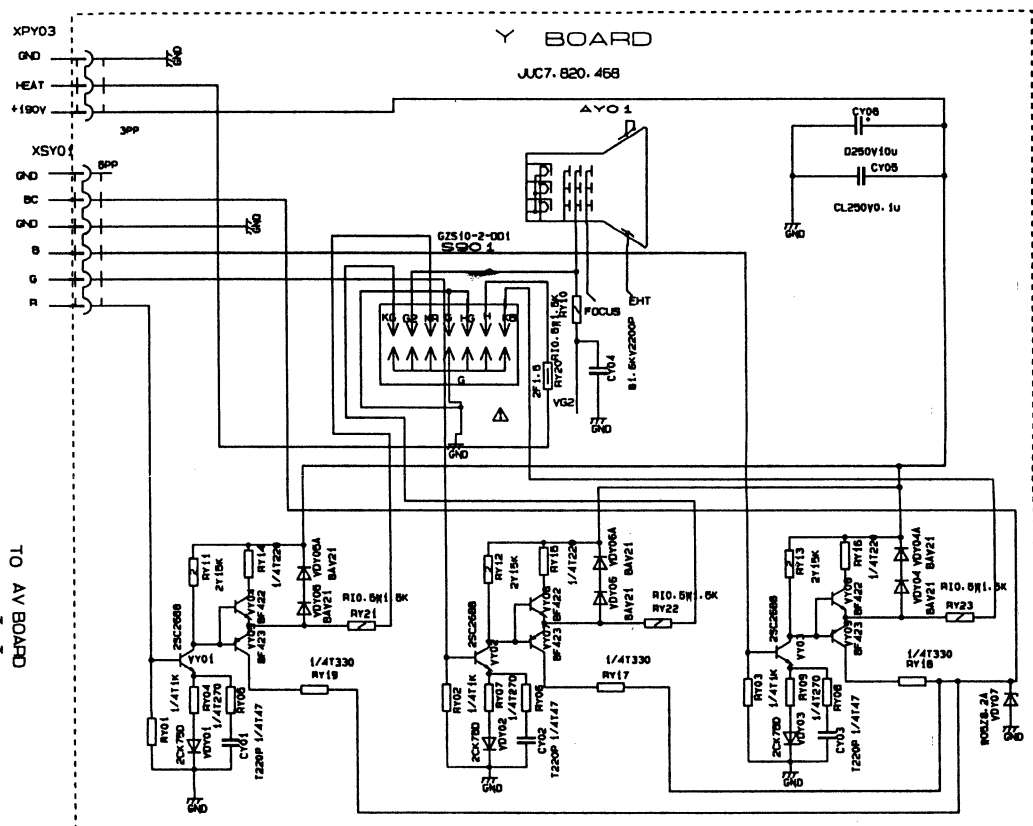
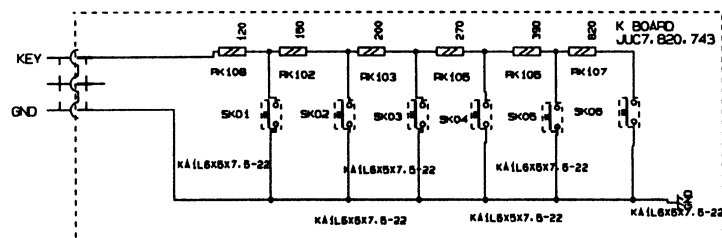
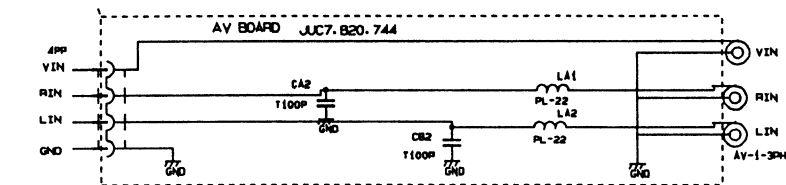
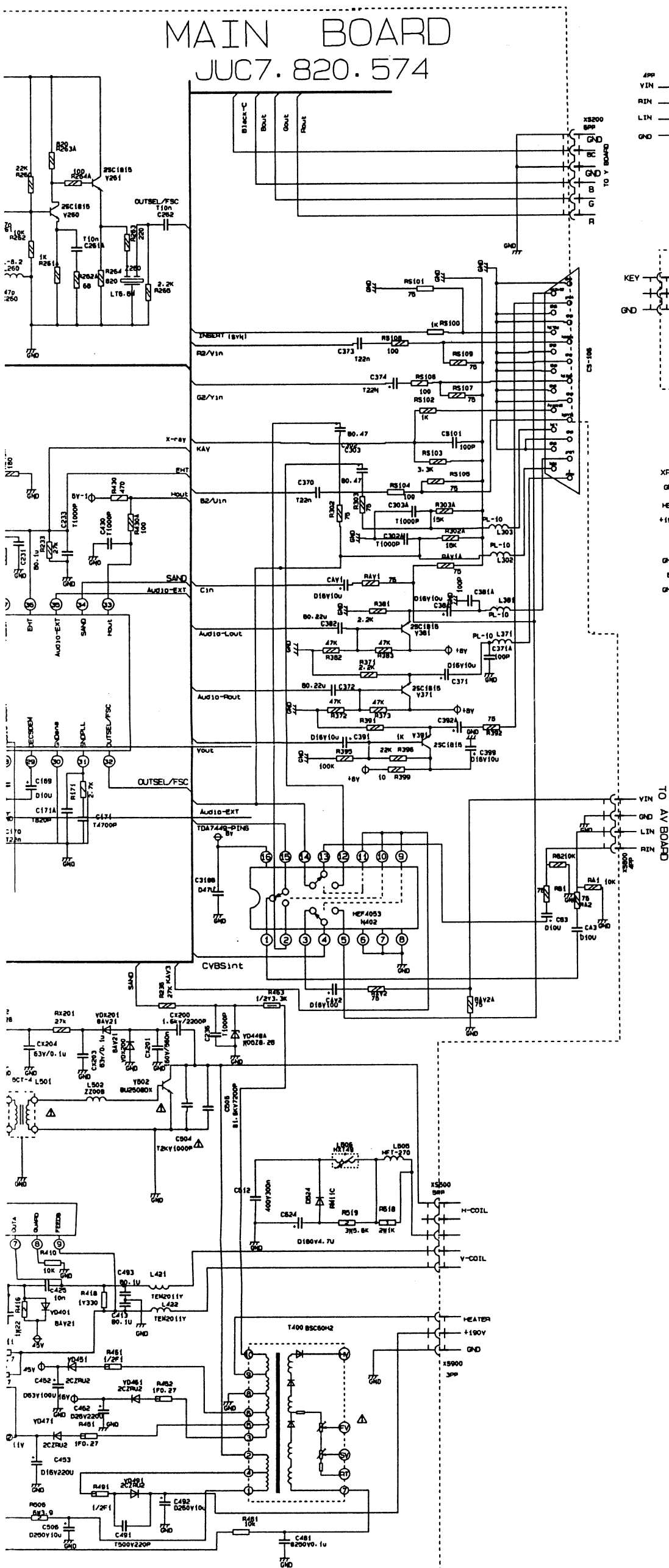
Many electrical and mechanical components in this chassis have special safety-related characteristics. Components which have these special safety characteristics in this manual and its supplements are identified by the international hazard symbols or UL, FCC, FDA or VDE marking on the circuit diagram and parts list. When replacing any of these components, substitute the one which has the same safety characteristics as specified in the manual.

APPENDIX:

CIRCUIT DIAGRAM FOR 21C



1C99E/PF21C18/PF21C99



The circuit diagram is only for reference

Specifications are subject to change without notice

1. Any components identified by Δ have special safety-related characteristics. Use replacement components which have the same characteristics as the original parts.

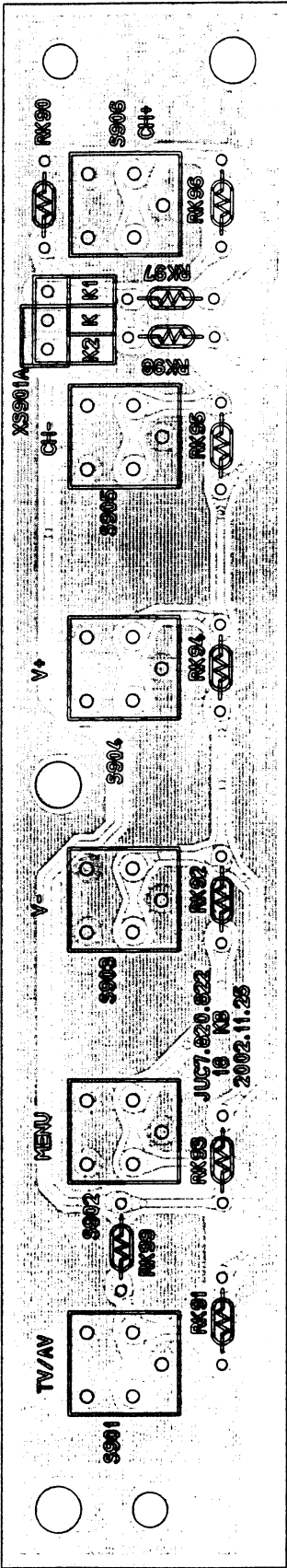
2. Cold ground ---

Hot ground ---

3. The component in shadows has special safety performance. Replace it with the same specifications as the original's

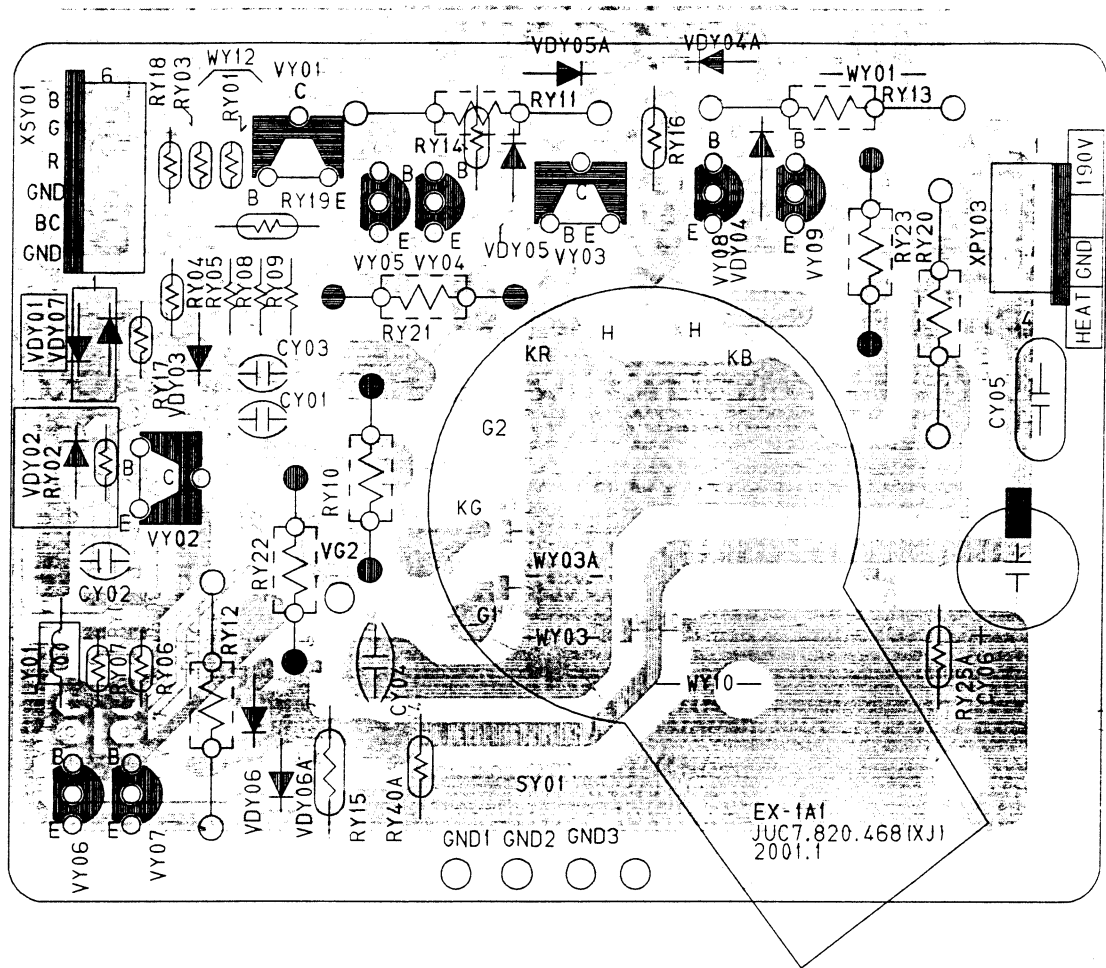
PRINTED CIRCUIT BOARD DIAGRAM (1/2)

REMOTE SENSOR PCB FOR PF21C18



PRINTED CIRCUIT BOARD DIAGRAM (2/2)

CRT RGB PCB FOR 21C99E/PF21C18/PF21C99



REMOTE SENSOR PCB FOR PF21C99

CONTROL PCB FOR PF21C99

